

**Claims Amendment**

1. (currently amended) A method of forming bumps on a silicon wafer having an active surface with a passivation layer and a plurality of bonding pads thereon, wherein the passivation layer exposes the bonding pads, the method comprising the steps of:

    forming an adhesion layer over the active surface of the wafer, wherein the adhesion layer covers both the bonding pads and the passivation layer;

    forming a barrier layer over the adhesion layer;

    forming a wettable layer over the barrier layer;

    conducting a photolithographic process to form a plurality of photoresist blocks over the wettable layer;

    conducting an etching operation to remove the wettable layer, the barrier layer and the adhesion layer outside the photoresist blocks so that only the wettable layer, the barrier layer and the adhesion layer underneath the photoresist blocks remain;

    removing the photoresist blocks;

    attaching a plurality of first solder blocks to the upper surface of the wettable layer through a bonding operation, wherein each first solder block has an upper surface and a lower surface such that the lower surface of the first solder block bonds with the wettable layer;

    planarizing the upper surface of the first solder blocks;

S10	11232	card near10 (reader reading) same (credit debit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 11:24
S11	386	S6 and S10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 11:24
S12	365	S11 and display\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 11:25
S13	148	S11 and display\$4 near20 instruct\$\$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 11:27
S15	98	S13 and @ad<="20030721"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 11:27
S16	18	("4701600"   "4825054"   "4851651"   "4900906"   "4965437"   "5278395"   "5564857"   "5687087"   "5753897"   "5949046").PN. OR ("6129275"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/19 13:34

7. (currently amended) The method of claim 1, wherein material constituting the second solder blocks is selected from ~~a~~the group consisting of lead-tin alloy, tin-silver alloy, tin-silver-copper alloy and tin.

8. (original) The method of claim 1, wherein the step of planarizing the upper surface of the first solder blocks includes polishing.

9. (original) The method of claim 1, wherein the step of planarizing the upper surface of the first solder blocks includes applying a pressure.

10. (original) The method of claim 1, wherein the step of attaching a first solder block to the wettable layer includes the sub-steps of:

providing a conductive wire;

heating one end of the conductive wire so that the heated end of the conductive wire transforms into a spherical blob;

pulling the spherical blob towards the wettable layer and pressing the spherical blob against the surface of the wettable layer; and

detaching the remaining portion of the conductive wire from the spherical blob to form the first solder block.

11. (original) The method of claim 10, wherein the step of pressing the spherical blob against the wettable layer is further assisted by application of ultrasound.

12. (original) The method of claim 1, wherein the step of attaching a second solder block to a first solder block further includes the sub-steps of:

providing a conductive wire;  
heating one end of the conductive wire so that the heated end of the conductive wire transforms into a spherical blob;  
pulling the spherical blob towards the first solder block and pressing the spherical blob against the upper surface of the first solder block; and  
detaching the remaining portion of the conductive wire from the spherical blob to form the second solder block.

13. (currently amended) The method of claim 12, wherein the step of pressing the spherical blob against the first solder block is further assisted by an application of ultrasound.

14. (currently amended) A method of forming bumps on ~~the~~an active surface of a silicon wafer, the method comprising the steps of:

forming an under-ball metallic layer over the active surface of the wafer, wherein the under-ball metallic layer is a composite layer;

removing a portion of the under-ball metallic layer to expose the active surface of the wafer;

attaching a plurality of first solder blocks to ~~the~~an upper surface of the under-ball metallic layer, wherein each first solder block has an upper surface and a lower surface such that the lower surface of each first solder block bonds with the under-ball metallic layer;

planarizing the upper surface of the first solder blocks; ~~and~~

attaching a second solder block to the upper surface of each first solder block; and

conducting a reflow operation to transform the first solder block and the second solder block into a single integral solder bump.

15. (original) The method of claim 14, wherein the step of forming an under-ball metallic layer over the active surface of the wafer includes the sub-steps of:

forming an adhesion layer over the active surface of the wafer;  
forming a barrier layer over the adhesion layer; and  
forming a wettable layer over the barrier layer.

16. (original) The method of claim 15, wherein material constituting the adhesion layer is selected from a group consisting of titanium, titanium-tungsten alloy, aluminum and chromium.

17. (original) The method of claim 15, wherein material constituting the barrier layer is selected from a group consisting of nickel-vanadium alloy, chromium-copper alloy and nickel.

18. (original) The method of claim 15, wherein material constituting the wettable layer is selected from a group consisting of copper, palladium and gold.

19. (currently amended) The method of claim 14, wherein each second solder block includes an upper surface and a lower surface, the lower surface is in contact with the upper surface of the first solder block, and the upper surface of the second solder block is planarized by polishing followed by ~~a reflow~~ the reflow operation after the second solder block is bonded to the first solder block.

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20. (currently amended) The method of claim 14, wherein material constituting the first solder blocks is selected from ~~the~~ the group consisting of lead-tin alloy, lead-silver alloy, tin-silver alloy, silver and gold.

21. (currently amended) The method of claim 14, wherein material constituting the second solder blocks is selected from ~~the~~ the group consisting of lead-tin alloy, tin-silver alloy, tin-silver-copper alloy and tin.

22. (original) The method of claim 14, wherein the step of planarizing the upper surface of the first solder blocks includes polishing.

23. (original) The method of claim 14, wherein the step of planarizing the upper surface of the first solder blocks includes applying a pressure.

24. (original) The method of claim 14, wherein the step of attaching a first solder block to the under-ball metallic layer includes the sub-steps of:

providing a conductive wire;

heating one end of the conductive wire so that the heated end of the conductive wire transforms into a spherical blob;

pulling the spherical blob towards the under-ball metallic layer and pressing the spherical blob against the surface of the under-ball metallic layer; and

detaching the remaining portion of the conductive wire from the spherical blob to form the first solder block.

25. (original) The method of claim 24, wherein the step of pressing the spherical blob against the under-ball metallic layer is further assisted by application of ultrasound.

26. (original) The method of claim 14, wherein the step of attaching a second solder block to a first solder block further includes the sub-steps of:

providing a conductive wire;

heating one end of the conductive wire so that the heated end of the conductive wire transforms into a spherical blob;

pulling the spherical blob towards the first solder block and pressing the spherical blob against the upper surface of the first solder block; and

detaching the remaining portion of the conductive wire from the spherical blob to form the second solder block.

27. (original) The method of claim 26, wherein the step of pressing the spherical blob against the first solder block is further assisted by application of ultrasound.

28. (currently amended) A method of forming bumps on the active surface of a silicon wafer, wherein the active surface further includes an under-ball metallic layer thereon and the under-ball metallic layer is a composite layer that comprises a plurality of material layers, the method comprising the steps of:

attaching at least one first solder block to ~~the~~<sup>an</sup> upper surface of the under-ball metallic layer by conducting a bonding operation; ~~and~~

attaching at least one second solder block to the upper surface of the first solder block by conducting a bonding operation; and

integrating at least one of the first solder block and at least one of the second solder block into a single solder bump by performing a reflow operation.

29. (original) The method of claim 28, wherein after the step of attaching the first solder block to the upper surface of the under-ball metallic layer, further includes planarizing the upper surface of the first solder block and attaching a second solder block to the upper surface of the first solder block by conducting a bonding operation.

30. (original) The method of claim 29, wherein the step of planarizing the upper surface of the first solder block includes polishing.

31. (original) The method of claim 29, wherein the step of planarizing the upper surface of the first solder block include pressing.

32. (original) The method of claim 28, wherein after the step of attaching the second solder block to the upper surface of the first solder block, further includes planarizing the upper surface of the second solder block.

33. (original) The method of claim 32, wherein the step of planarizing the upper surface of the second solder blocks includes polishing.

34. (currently canceled)

35. (currently amended) The method of claim 28, wherein material constituting the first solder blocks is selected from ~~a~~the group consisting of lead-tin alloy, lead-silver alloy, tin-silver alloy, silver and gold.

36. (currently amended) The method of claim 28, wherein material constituting the second solder blocks is selected from ~~a~~the group consisting of lead-tin alloy, tin-silver alloy, tin-silver-copper alloy and tin.

37. (original) The method of claim 28, wherein the step of attaching a first solder block to the under-ball metallic layer includes the sub-steps of:

providing a conductive wire;

heating one end of the conductive wire so that the heated end of the conductive wire transforms into a spherical blob;

pulling the spherical blob towards the under-ball metallic layer and pressing the spherical blob against the surface of the under-ball metallic layer; and

detaching the remaining portion of the conductive wire from the spherical blob to form the first solder block.

38. (original) The method of claim 37, wherein the step of pressing the spherical blob against the under-ball metallic layer is further assisted by application of ultrasound.

39. (original) The method of claim 28, wherein the step of attaching a second solder block to a first solder block further includes the sub-steps of:

providing a conductive wire;

heating one end of the conductive wire so that the heated end of the conductive wire transforms into a spherical blob;

pulling the spherical blob towards the first solder block and pressing the spherical blob against the upper surface of the first solder block; and

detaching the remaining portion of the conductive wire from the spherical blob to form the second solder block.

40. (original) The method of claim 39, wherein the step of pressing the spherical blob against the first solder block is further assisted by application of ultrasound.

41. (newly added) The method of claim 1, wherein the reflow operation is conducted at a temperature higher than an alloying temperature between the first solder block and the second solder block.

42. (newly added) The method of claim 14, wherein the reflow operation is conducted at a temperature higher than an alloying temperature between the first solder block and the second solder block.

43 (newly added) The method of claim 1, wherein the reflow operation is conducted at a temperature higher than an alloying temperature between the first solder block and the second solder block.